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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

# U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>AC3</i> Q.A. <i>3</i>	

APPLICATION NO. 09/911116	CONT/PRIOR D	CLASS 257	SUBCLASS 757	ART UNIT 2814 2827	EXAMINER GRIMM
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## APPLICANTS

Mirmajid Seyyedy

# TITLE

## Flip chip technique for chip assembly

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				<b>ISSUE FEE</b>	
	_____ (Primary Examiner) _____ (Date)	Amount Due	Date Paid		
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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